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**Application Information**

Title Line One:: Semiconductor Device And Method Of  
Title Line Two:: Manufacture Thereof, Circuit Board  
Title Line Three:: And Electronic Instrument  
Title Line Four::

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**Continuity Information**

>This application is a:: U.S. National Stage of  
Application One:: PCT/JP99/03417  
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Patent Number::  
which is a::  
>>Application Two::  
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**Prior Foreign Applications**

Foreign Application One:: 10-201246  
Filing Date:: July 1, 1998  
Country:: Japan  
Priority Claimed:: yes  
Foreign Application Two::  
Filing Date::  
Country::  
Priority Claimed::  
Foreign Application Three::  
Filing Date::  
Country::  
Priority Claimed::